



全漢企業股份有限公司

電氣規格書

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 研發部門 : RD8
 作者 : evalee/李穎萱
 Model No/Type : ,FSP700-80GLN,PC,SSL,B,W/NK,W/IO,WO/O,WO/SS,FSP,W/PFC(A),5L1S,OEM,IN
 TEL,FULL RANGE,RD8
 機密 : N
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單位	姓名	單位	姓名	單位	姓名
安規 1	Chow 周懿慧	主管	harding 施永祥		

相關料號：

9PB7000111 ,G.P,AO700-12ALN,(A279),PC,SSI,2
BALL,W/NK,W/IO,WO/O,WO/SS,AOPEN,W/PFC(A),5L1S,OEM,INTEL,FULL
RANGE,9PB7000101,RD8

9PB7000107 KEEP,G.P,AO700-12ALN,(A276),PC,ATX,2
BALL,W/NK,W/IO,WO/O,WO/SS,AOPEN,W/PFC(A),5L1S,INTEL,FULL
RANGE,9PB7000104,W/NK,RD8,RM

9PB7000109 KEEP,G.P,AO700-12ALN,(A278),PC,ATX,2
BALL,W/NK,W/IO,WO/O,WO/SS,AOPEN,W/PFC(A),5L1S,OEM,INTEL,FULL
RANGE,9PB7000101,RD8,RM

9PB7000102 KEEP,G.P,AO700-12ALN,(A272),PC,SSI,2
BALL,W/NK,W/IO,WO/O,WO/SS,AOPEN,W/PFC(A),5L1S,OEM,INTEL,FULL
RANGE,9PB7000101,RD8,RM

9PB7000104 ,G.P,AO700-12ALN,(A273),PC,ATX,2
BALL,W/NK,W/IO,WO/O,WO/SS,AOPEN,W/PFC(A),5L1S,OEM,INTEL,FULL
RANGE,9PB7000101,RD8,RM

9PB7000105 KEEP,G.P,AO700-12ALN,(A274),PC,ATX,2
BALL,W/NK,W/IO,WO/O,WO/SS,AOPEN,W/PFC(A),5L1S,OEM,INTEL,FULL
RANGE,9PB7000101,RD8,RM

9PB7000108 KEEP,G.P,AO700-12ALN,(A277),PC,ATX,2
BALL,W/NK,W/IO,WO/O,WO/SS,AOPEN,W/PFC(A),5L1S,OEM,INTEL,FULL
RANGE,RD8,RM

9PB7000112 ,G.P,AO700-12ALN,(A27A),PC,ATX,2
BALL,W/NK,W/IO,WO/O,WO/SS,AOPEN,W/PFC(A),5L1S,OEM,INTEL,FULL
RANGE,9PB7000104,RD8,RM

9PB7000101 KEEP,G.P,AO700-12ALN,(A271),PC,SSI,2
BALL,W/NK,W/IO,WO/O,WO/SS,AOPEN,W/PFC(A),5L1S,OEM,INTEL,FULL
RANGE,9PB7000100,RD8

9PB7000100
,G.P,FSP700-80GLN,PC,SSI,SLEEVE,W/NK,W/IO,WO/O,WO/SS,FSP,W/PFC(A),5L1S,OEM
,INTEL,FULL RANGE,RD8

9PB7000103 ,G.P,FX700-GLN,(RMB1),PC,ATX,2
BALL,W/NK,W/IO,WO/O,WO/SS,FSP,W/PFC(A),6L2S,OEM,INTEL,FULL RANGE,SPH 外
購,9PB7000100,RD8,RM

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SPECIFICATION

FSP700-80GLN

Main Feature:

**Meet 80Plus
High Efficiency
Active PFC Circuit
Full Range Input**

**DEC 14,2006
REV:1.01**



全漢企業股份有限公司
SPI Electronic Co.,Ltd .

MODEL: FSP700-80GLN

Revision History

<u>Rev</u>	<u>Description</u>	<u>Date</u>	<u>Author</u>
1.00		2005/03/14	
1.01	修改 4.1 OUTPUT VOLTAGE AND CURRENT RATING	2006/12/14	

1. GENERAL DESCRIPTION AND SCOPE

This is the specification of Model FSP700-80GLN; AC-line powered switching power supply with active PFC (Power Factor Correction) circuit, meet EN61000-3-2 and with Full Range Input features.

The specification below is intended to describe as detailedly as possible the functions and performance of the subject power supply. Any comment or additional requirements to this specification from our customers will be highly appreciated and treated as a new target for us to approach.

2. REFERENCE DOCUMENTS

The subject power supply will meet the EMI requirements and obtain main safety approvals as following:

2.1 EMI REGULATORY

- FCC Part 15 Subpart J, Class 'B' 115 Vac operation.
- CISPR 22 Class 'B' 230 Vac operation.

2.2 SAFETY

- NEMKO EN 60950
- VDE EN60950
- CSA-C22.2 NO. 60950
- IEC 60950
- UL 60950
- CE :
 - EN 55022:1998+A1: 2000, Class B
 - EN 61000-3-2: 2000
 - EN 61000-3-3: 1995+A1: 2001
- CISPR22: 1997+A1: 2000, Class B
- AS/NZS CISPR 22: 2002, Class B

3. INPUT ELECTRICAL SPECIFICATIONS

3.1 AC INPUT

Parameter	Min.	Nom. ⁽¹⁾	Max.	Unit
V _{in} (115VAC)	99	115	150	VAC _{rms}
V _{in} Frequency	57	--	63	HZ
V _{in} (230VAC)	150	230	265	VAC _{rms}
V _{in} Frequency	47	--	53	HZ

◆ Nominal voltages for test purposes are considered to be within $\pm 1.0V$ of nominal.

3.2. INRUSH CURRENT

Maximum inrush current from power-on (with power on at any point on the AC sine) and including, but not limited to, three line cycles, shall be limited to a level below the surge rating of the input line cord, AC switch if present, bridge rectifier, fuse, and EMI filter components. Repetitive ON/OFF cycling of the AC input voltage should not damage the power supply or cause the input fuse to blow.

3.3. INPUT LINE CURRENT & POWER FACTOR

AC input	Input line current	Power Factor
115V	< 8Amps – rms	> 0.95
230V	< 4Amps – rms	> 0.9

3.4 EFFICIENCY

3.4.1 General

Under the load conditions defined in Table 1 and Table 2. The loading condition for testing efficiency shown in Table 1 represents a fully loaded system. a ~ 50-60% (typical) loaded system. and a ~ 20-30% (light) loaded system.

Table. 1 Loading Table for Efficiency Measurements

700W(loading shown in Amps)								
Loading	5V	3.3V	12V1	12V2	12V3	12V4	-12V	+5Vsb
Full	10	6	14	14	11.6	12	0.5	1
Typical	5	3	8	8	7	7	0.3	0.5
Light	0.5	0.8	5	5	3	3	0.1	0.1

Table 2. Minimum Efficiency Vs Load

Loading	Full load	Typical load	Light load
115V Required Minimum Efficiency	80%	81%	80%
230V Required Minimum Efficiency	81%	82%	81%

3.5 MECHANICAL SPECIFICATIONS

The mechanical drawing of the subject power supply, which indicate the form factor, location of the mounting holes, location, the length of the connectors, and other physical specifications of the subject power supply. Please refer to the attachment drawing.

4.0. OUTPUT ELECTRICAL REQUIREMENTS

4.1 OUTPUT VOLTAGE AND CURRENT RATING

Output	MINIMUM LOAD	NORMAL LOAD	MAXIMUM LOAD	LOAD REG	LINE REG.	RIPPLE&NOISE
+3.3 V	0.8A	18A	36A	±5%	±1%	100mV P-P
+5 V	0.5A	15A	30A	±5%	±1%	100mV P-P
+12V1	0A	9A	18A	±5%	±1%	200mV P-P
+12V2	0A	9A	18A	±5%	±1%	200mV P-P
+12V3	0.9A	9A	18A	±5%	±1%	200mV P-P
+12V4	0.1A	9A	18A	±5%	±1%	200mV P-P
-12V	0A	0.25	0.5A	±10%	±1%	200mV P-P
+5VSB	0.1A	1.0A	3.0 A	±5%	±1%	100mV P-P

- (1) +3.3V & 5V total output not exceed 155W.
- (2) total output current for this subject power supply is 70A
- (3) maximum combined current for the 12V outputs shall be 50A
- (4) total output for this subject power supply is 700W watts
- (5) Voltages and ripple are measured at the load side of mating connectors with a 0.1uF monolithic ceramic capacitor paralleled by a 10uF electrolytic capacitor across the measuring terminals.

4.2. LOAD CAPACITY SPECIFICATIONS

The cross regulation defined as follows, the voltage regulation limits DC include DC Output ripple & noise.

LOAD	+3.3V	+5V	12V1	12V2	12V3	12V4	-12V	+5VSB
Conditin_1	x	x	x	x	x	x	x	3A
Conditin_2	0.8A	0.5A	0A	0A	0.9A	0.1A	0A	0.1A
Conditin_3	0.8A	0.5A	0A	0A	0.9A	0.1A	0.5A	0.1A
Conditin_4	1.5A	2A	4.5A	4.5A	18A	18A	0.1A	0.1A
Conditin_5	1.5A	14A	2.5A	2.5A	2.5A	2.5A	0.1A	0.1A
Conditin_6	1.5A	30A	10A	10A	9A	9A	0.1A	0.1A
Conditin_7	10A	10A	18A	18A	7A	7A	0.1A	2A
Conditin_8	36A	7.2A	6.7A	6.7A	6.5A	6.6A	0.1A	0.1A
Conditin_9	36A	1A	1A	1A	1A	1A	0A	0.1A

4.3. HOLD-UP TIME (@ 90% FULL LOAD)

115V / 60Hz : 17 mSec. Minimum.

230V / 50Hz : 17 mSec. Minimum.

The output voltage will remain within specification, in the event that the input power is removed or interrupted, for the duration of one cycle of the input frequency. The interruption may occur at any point in the AC voltage cycle. The power good signal shall remain high during this test.

4.4.OUTPUT RISE TIME

(10% TO 95% OF FINAL OUTPUT VALUE, @FULL LOAD)

115V-rms or 230V-rms	+ 5Vdc/3.3Vdc : 20ms Maximum
115V-rms or 230V-rms	+ 12Vdc : 20ms Maximum
115V-rms or 230V-rms	+ 5Vsb : 25ms Maximum
115V-rms or 230V-rms	- 12Vdc : 20ms Maximum

4.5.OVER VOLTAGE PROTECTION

Voltage Source	Protection Point
+3.3V	3.76V-4.8V
+5V	5.6V-7.0V
12VDC	13.5V-16.5V

4.6.OVE-CURRENT PROTECTION

OUTPUT VOLTAGE	Max. overcurrent limit
+3.3V	60A
+5V	48A
12V1	22A
12V2	22A
12V3	22A
12V4	22A

4.7.SHORT CIRCUIT PROTECTION

Output short circuit is defined to be a short circuit load of less than 0.1 ohm.

In the event of an output short circuit condition on +3.3V, +5V ,+12V1,+12V2,+12V3 or 12V4 output, the power supply will shutdown and latch off without damage to the power supply.The power. supply shall return to normal operation after the short circuit has been removed and the power switch has been turned off for no more than 2 seconds.

In the event of an output short circuit condition on -12V output, the power supply will not be damaged. The power supply shall return to normal operation as soon as the short circuit has been removed. and the power switch has been turned off for no more than 2 seconds.

4.8. POWER SIGNAL

POWER GOOD @ 115/230V,FULL LOAD	100 –500mSec.
POWER FAIL @115/230V, FULL LOAD	1 mSec. minimum

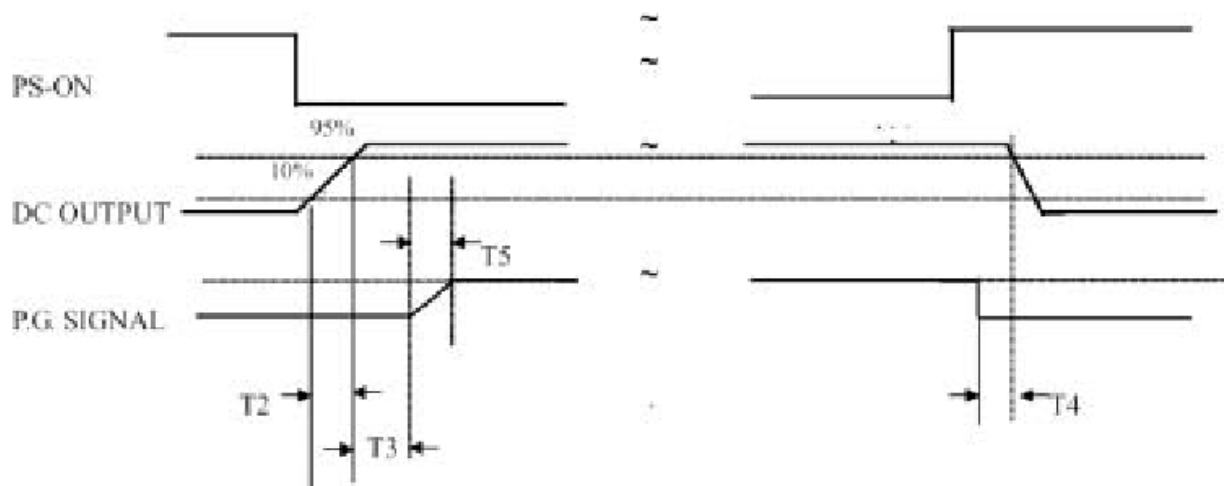


Figure 1

T2 : RISE TIME < 20mS

T3 : POWER GOOD DELAY TIME 100mS-500mS

T4 : POWER FAIL DELAY TIME > 1mS

T5 : POWER GOOD RISE TIME ≤ 10mS

5.0 ENVIRONMENTAL REQUIREMENTS

The power supply will be compliant with each item in this specification for the following Environmental conditions.

5.1. TEMPERATURE RANGE

Operating	0 to + 50 deg. C
Storage	-20 to +80 deg. C

The maximum continuous power rating of supply is 700W at 25°C .

De-rate 8W/°C from 25°C to 50°C .

The maximum continuous power rating of supply is 500W at 50°C .

5.2. HUMIDITY

Operating	5 –95% RH, Non-condensing
Storage	5 –95% RH, Non-condensing

5.3. VIBRATION

The subject power supply will withstand the following imposed conditions without experiencing non-recoverable failure or deviation from specified output characteristics.

Vibration Operating – Sine wave excited, 0.25 G maximum acceleration, 10-250 Hz swept at one octave / min. Fifteen minute dwell at all resonant points, where resonance is defined as those exciting frequencies at which the device under test experiences excursions two times large than non-resonant excursions.

Plane of vibration to be along three mutually perpendicular axes.

5.4 GROUND LEAKAGE CURRENT

The power supply ground leakage current shall be less than 3.5 mA.

5.5 RELIABILITY

The power supply reliability, when calculated by MIL-HDBK-217; latest revision, are exceed 100,000 hours with all output at maximum load and an ambient temperature of 25°C .

5.6 DIELECTRIC STRENGTH

Primary to Frame Ground : 1800 Vac for 1 sec.

Primary to Secondary : 1800Vac for 1 sec

5.7 INSULATION RESISTANCE

Primary to Frame Ground : 20 Meg.ohms Minimum

Primary to Secondary : 20 Meg.ohms Minimum

6.0. LABELLING

Label marking will be permanent, legible and complied with all agency requirements.

6.1. MODEL NUMBER LABEL

Labels will be affixed to the sides of the power supply showing the following:

- Manufacturer's name and logo.
- Model no., serial no., revision level, location of manufacturer.
- The total power output and the maximum load for each output.
- AC input rating.